

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Li Attorney Docket: 2003 P 54309 US  
Filed: Herewith Examiner: TBD  
Serial No.: TBD Art Unit: TBD  
For: Transistor with Silicon and Carbon Layer in the Channel Region

Mail Stop: Patent Application  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

December 30, 2003  
Date

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Respectfully submitted,



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| <b>Substitute for form 1449A/PTO</b><br><br><b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br><br><i>(use as many sheets as necessary)</i> |   |    |   | <b>Complete if Known</b> |                 |
|  |   |    |   | Application Number       | TBD             |
|  |   |    |   | Filing Date              | HEREWITH        |
|  |   |    |   | First Named Inventor     | Li              |
|  |   |    |   | Art Unit                 | TBD             |
|  |   |    |   | Examiner Name            | TBD             |
| Sheet  | 1 | of | 1 | Attorney Docket Number   | 2003 P 54309 US |

| U.S. PATENT DOCUMENTS |                       |  |                                |  |   |
|-----------------------|-----------------------|--|--------------------------------|--|---|
| Examiner Initials*    | Cite No. <sup>1</sup> | Document Number                            | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines, Where<br>Relevant Passages or Relevant<br>Figures Appear |
|                       |                       | Number - Kind Code <sup>2</sup> (if known) |                                |  |   |
|                       | 1                     | US-6,621,131 B2                            | 09-16-2003                     | Murthy, <i>et al.</i>                              |   |
|                       |                       | US-  |                                |  |   |
|                       |                       | US-  |                                |  |   |
|                       |                       | US-  |                                |  |   |
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| FOREIGN PATENT DOCUMENTS |                       |   |                                |  |   |                |
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| Examiner Initials*       | Cite No. <sup>1</sup> | Foreign Patent Document   | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines,<br>Where Relevant Passages<br>or Relevant Figures Appear | T <sup>6</sup> |
|                          |                       | Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known) |                                |  |   |                |
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| OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS |                       |   |                |
|--|-----------------------|---|----------------|
| Examiner Initials*                                 | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|  |                       |   |                |
|  | 2                     | ERNST, T., <i>et al.</i> , "A New Si:C Epitaxial Channel nMOSFET Architecture with Improved Drivability and Short-Channel Characteristics," 2003 Symposium on VLSI Technology Digest of Technical Papers, 4-89114-035-6/03.                                     |                |
|  |                       |   |                |

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|--------------------|--|-----------------|--|
| Examiner Signature |  | Date Considered |  |
|--------------------|--|-----------------|--|

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